

FORM PTO-1449

Attorney Docket:

Application No.:

18153.0042

09/870,457

INFORMATION DISCLOSURE CITATION

Applicant:

Examiner:

Michael I.

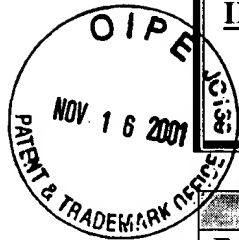
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Filing Date:

Group Art Unit:

June 1, 2001

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**U.S. PATENT DOCUMENTS**

Examiner Initial	Ref. No	Document Number	Date	Name	Class	Sub-Class	Filing Date
Bse		6,061,780	May 9, 2000	Shippey, et al			
		5,056,004	Oct. 8, 1991	Ohde et al.			
		6,026,489	Feb. 15, 2000	Wachi et al.			
		5,951,679	Sept. 14, 1999	Anderson, et al.			
		5,117,498	May 26, 1992	Miller, et al.			
		5,101,484	Mar. 31, 1992	Kohn			
		5,740,419	April 14, 1998	Potter			
		5,826,096	Oct. 20, 1998	Baxter			
		5,938,759	Aug. 17, 1999	Kamijo			
		5,930,503	July 27, 1999	Drees			
		6,076,154	June 13, 2000	Van Eijndhoven et al			
		5,706,460	Jan. 6, 1998	Craig et al.			
		5,825,730	Oct. 20, 1998	Nishida et al.			
		6,014,723	Jan 11, 2000	Tremblay et al.			
		5,790,443	Aug. 4, 1998	Shen et al.			
		5,448,706	Sept. 5, 1995	Fleming et al.			
		5,448,703	Sept. 5, 1995	Amini et al.			
		5,206,940	Apr. 27, 1993	Murakami et al.			
		4,829,420	May 9, 1989	Stahle			
		5,778,416	Jul. 7, 1998	Harrison et al.			
		5,600,813	Feb. 4, 1997	Nakagawa et al.			
		5,012,441	April 30, 1991	Retter			
		6,009,454	Dec. 28, 1999	Dummermuth			
		5,828,875	Oct. 27, 1998	Halvarsson et al.			
		4,882,701	Nov. 21, 1989	Ishii			
		4,626,988	Dec. 2, 1986	George			
		5,099,445	Mar. 24, 1992	Studor et al.			
		5,379,240	Jan. 3, 1995	Byrne			
		5,327,543	Jul. 5, 1994	Miura et al.			
		4,511,990	April 16, 1985	Hagiwara et al.			
		4,829,460	May 9, 1989	Ito			
		5,038,310	Aug. 6, 1991	Akagiri et al.			
		4,872,128	Oct. 3, 1989	Shimizu			
Examiner				Date Considered	4/16/04		

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Examiner Initial	Ref. No	Document Number	Date	Name	Class	Sub-Class	Filing Date
B30		4,977,533	Dec. 11, 1990	Miyabayashi et al.			
		5,941,940	Aug. 24, 1999	Prasad et al.			
		5,951,627	Sept. 14, 1999	Kiamilev et al.			
		6,058,409	May 2, 2000	Kozaki et al.			
		5,991,787	Nov. 23, 1999	Abel et al.			
		5,007,020	Apr. 9, 1991	Inskeep			
		5,197,140	Mar. 23, 1993	Balmer			
		5,499,380	Mar. 12, 1996	Iwata et al.			
		4,839,846	Jun. 13, 1989	Hirose et al.			
		4,941,120	Jul. 10, 1990	Brown et al.			
		6,058,410	May 2, 2000	Sharangpani			
		5,943,249	Aug. 24, 1999	Handlogten			
		5,212,662	May 18, 1993	Cocanougher et al.			
		6,101,521	Aug. 8, 2000	Kosiec			
		4,943,940	Jul. 24, 1990	New			
		6,134,574	Oct. 17, 2000	Oberman et al.			
		5,930,159	Jul. 27, 1999	Wong			
		5,892,697	Apr. 6, 1999	Brakefield			
		5,619,711	Apr. 8, 1997	Anderson			
		5,917,741	Jun. 29, 1999	Ng			
		5,909,385	Jun. 1, 1999	Nishiyama et al.			
		5,276,634	Jan. 4, 1994	Suzuki et al.			
		5,880,984	Mar. 9, 1999	Burchfiel et al.			
		5,469,377	Nov. 21, 1995	Amano			

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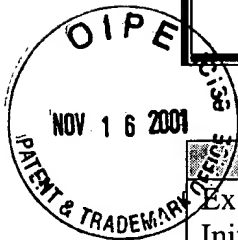
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Examiner Initial	Ref. No	Document Number	Date	Name	Class	Sub-Class	Filing Date
BJO		6,145,049	Nov. 7, 2000	Wong			
		6,128,728	Oct. 3, 2000	Dowling			
		5,327,566	July 5, 1994	Forsyth			
		5,642,516	Jun. 24, 1997	Hedayat et al.			
		3,781,810	Dec. 25, 1973	Downing			
		5,463,749	Oct. 31, 1995	Wertheizer et al			
		5,471,600	Nov. 28, 1995	Nakamoto			
		5,737,570	Apr. 7, 1998	Koch			
		4,481,576	Nov. 6, 1984	Bicknell			
		4,959,776	Sep. 25, 1990	Deerfield et al.			
		5,032,986	Jul. 16, 1991	Pathak et al.			
		5,155,823	Oct. 13, 1992	Tsue			
		6,044,434	Mar. 28, 2000	Oliver			
		4,984,213	Jan. 8, 1991	Abdoo et al.			
		6,058,464	May 2, 2000	Taylor			
		6,145,049	Nov. 7, 2000	Wong			
		5,327,566	July 5, 1994	Forsyth			
		5,642,516	June 24, 1997	Hedayat, et al.			
		6,128,728	Oct. 3, 2000	Dowling			
		5,122,981	Jun. 16, 1992	Taniguchi			
		5,812,439	Sep. 22, 1998	Hansen			
		5,696,711	Dec. 9, 1997	Makineni			
		5,808,926	Sept. 15, 1998	Gorshtein et al.			
		5,694,350	Dec. 2, 1997	Wolrich et al.			
		5,568,412	Oct. 22, 1996	Han et al.			
		5,764,555	Jun. 9, 1998	McPherson et al.			
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		5,548,544	Aug. 20, 1996	Matheny et al.			

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B50		EP 0 554 917 A2	11-08-93	Europe			

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BSC		Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A, no. 5, May 2001 (2001-05), pages 1339-1347, XP001060025 ISSN: 0916-8508
BJO		Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, IS, vol. 37, no. 10, October 1998 (1998-10), pages 46, 48, 50-53, XP000860706 ISSN:0010-4566
BSC		Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, 2 March 1998 (1998-03-02), pages 73-74, 76, 78-80, XP000779113 ISSN: 0012-7515

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